

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No.: 09/713,137 Filing Date: November 14, 2000
Confirmation No.: Unknown
Applicant: Oleg Siniaguine et al.
Assignee: Tru-Si Technologies, Inc.
Examiner: Art Unit 1762
Docket No.: M-7554 US

COMMISSIONER FOR PATENTS
Washington, D.C. 20231

POWER OF ATTORNEY BY ASSIGNEE OF ENTIRE INTEREST

Sir:

The above-identified assignee hereby revokes all powers of attorney previously given and appoints the attorney(s) and/or agent(s) associated with the customer number below to transact all business in the United States Patent and Trademark Office in connection with this application:

Customer No. 32605

Please direct all correspondence regarding this application to the address associated with this customer number, and direct all telephone calls to:

Michael Shenker

Phone: (408) 392-9250

Fax: (408) 392-9262

ASSIGNEE CERTIFICATION UNDER 37 CFR 3.73(B)

The undersigned representative of the above-identified assignee certifies that the above-identified assignee is the assignee of the entire right, title and interest in the above-identified patent application by virtue of a chain of title from the inventor(s) of the above-identified patent application to the above-identified assignee as shown below:

1. From:

Siniaguine, Oleg
Savastiouk, Sergey
Halahan, Patrick
Kao, Sam

To: TruSi Technologies, LLC

The assignment document has been recorded at Reel/Frame 011339/0535.

2. From: TruSi Technologies, LLC

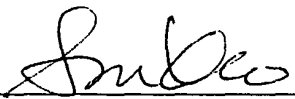
To: Tru-Si Technologies, Inc.

A copy of the assignment document is attached.

The undersigned (whose title is supplied below) is empowered to sign this certificate on behalf of the above-identified assignee.

Date: March 11, 2003

Tru-Si Technologies, Inc.

By: 
(Signature)

Name: Sam Kao
(Printed or Typed Name)

Title: Director of Applications Development

**DECLARATION FOR PATENT APPLICATION
AND POWER OF ATTORNEY**

As a below named inventor, I hereby declare that:

My residence, post office address and citizenship are as stated below adjacent to my name.

I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of subject matter (process, machine, manufacture, or composition of matter, or an improvement thereof) which is claimed and for which a patent is sought by way of the application entitled

Plasma Processing Comprising Three Rotational Motions of an Article Being Processed

which (check) ☒ is attached hereto.
☐ and is amended by the Preliminary Amendment attached hereto.
☐ was filed on as Application Serial No.
☐ and was amended on (if applicable).

I hereby state that I have reviewed and understand the contents of the above identified specification, including the claims, as amended by any amendment referred to above.

I acknowledge the duty to disclose information, which is material to patentability as defined in Title 37, Code of Federal Regulations, § 1.56.

I hereby claim foreign priority benefits under Title 35, United States Code, § 119(a)-(d) of any foreign application(s) for patent or inventor's certificate or any PCT international application(s) designating at least one country other than the United States of America listed below and have also identified below any foreign application(s) for patent or inventor's certificate or any PCT international application(s) designating at least one country other than the United States of America filed by me on the same subject matter having a filing date before that of the application(s) of which priority is claimed:

| Prior Foreign Application(s) | | | Priority Claimed | |
|------------------------------|---------|----------------------|--------------------------|--------------------------|
| Number | Country | Day/Month/Year Filed | Yes | No |
| N/A | | | <input type="checkbox"/> | <input type="checkbox"/> |

I hereby claim the benefit under Title 35, United States Code, § 119(e) of any United States provisional application(s) listed below:

| Provisional Application Number | Filing Date |
|--------------------------------|-------------|
| N/A | |

I hereby claim the benefit under Title 35, United States Code, § 120 of any United States application(s) or PCT international application(s) designating the United States of America listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior application(s) in the manner provided by the first paragraph of Title 35, United States Code, § 112, I acknowledge the duty to disclose information, which is material to patentability as defined in Title 37, Code of Federal Regulations, § 1.56, which became available between the filing date of the prior application(s) and the national or PCT international filing date of this application:

| Application Serial No. | Filing Date | Status (patented, pending, abandoned) |
|------------------------|-------------|---------------------------------------|
| N/A | | |

I hereby appoint the following practitioners to prosecute this application and to transact all business in the United States Patent and Trademark Office connected therewith:

Cust mer Number 24251



Please address all correspondence and telephone calls to:

Michael Shenker
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25 Metro Drive, Suite 700
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Telephone: 408-453-9200
Facsimile: 408-453-7979

I declare that all statements made herein of my own knowledge are true, all statements made herein on information and belief are believed to be true, and all statements made herein are made with the knowledge that whoever, in any matter within the jurisdiction of the Patent and Trademark Office, knowingly and willfully falsifies, conceals, or covers up by any trick, scheme, or device a material fact, or makes any false, fictitious or fraudulent statements or representations, or makes or uses any false writing or document knowing the same to contain any false, fictitious or fraudulent statement or entry, shall be subject to the penalties including fine or imprisonment or both as set forth under 18 U.S.C. 1001, and that violations of this paragraph may jeopardize the validity of the application or this document, or the validity or enforceability of any patent, trademark registration, or certificate resulting therefrom.

Full name of first joint inventor: Oleg Siniaguine

Inventor's Signature: [Signature]

Date: 11/14/00

Residence: San Jose, California

Post Office Address: 1247 Weibel Way
San Jose, CA 95125

Citizenship: Russia

Full name of second joint inventor: Sergey Savastiouk

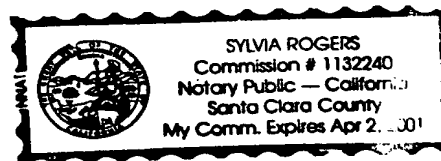
Inventor's Signature: [Signature]

Date: 11/14/00

Residence: San Jose, California

Post Office Address: 1627 Sparkling Way
San Jose, CA 95125

Citizenship: Russia



Full name of third joint inventor: Patriok Halahan

Inventor's Signature: Patriok Halahan

Date: 14 Nov 2000

Residence: ~~SAN MATEO~~ Sunnyvale, California

Post Office Address: ~~657 N. Pastoria Avenue~~

Citizenship: British

~~Sunnyvale, CA 94086~~ PH
3932 CASANOVA
SAN MATEO CA 94403

Full name of fourth joint inventor: Sam Kao

Inventor's Signature: Sam Kao

Date: 11-14-2000

Residence: San Mateo, California

Post Office Address: 1459 Parrott Drive
San Mateo, CA 94402

Citizenship: ~~Republic of China~~
USA SK.

ASSIGNMENT

For good and valuable consideration, receipt of which is hereby acknowledged, TruSi Technologies, LLC ("Assignor"), a limited liability company organized under the laws of California, hereby sells, assigns and transfers to Tru-Si Technologies, Inc. ("Assignee"), a California corporation, having a place of business at 657 N. Pastoria Avenue, Sunnyvale, California 94085, its successors and assigns, the Assignor's entire right, title and interest throughout the world in the inventions listed in the attached Schedule A, and all patent applications and patents of every country for said inventions, including divisions, reissues, continuations and extensions thereof, and all rights of priority resulting from the filing of said applications; the Assignor authorizes the Assignee to apply for patents of foreign countries for said invention, and to claim all rights of priority without further authorization from the Assignor; and agrees to execute all papers useful in connection with said United States and foreign applications, and generally to do everything possible to aid said Assignee, its successors, assigns and nominees, at their request and expense, in obtaining and enforcing patents for said inventions in all countries; and requests that the United States Patent and Trademark Office issue all patents granted for said inventions to the Assignee, its successors and assigns.

Executed this 11 day of February, 2003


Sergey Savastouk
Member Manager of TruSi Technologies, LLC

State of California)
) ss.
County of: Santa Clara)

On 2-11-03 before me, Cetta McCarthy (Notary Public)
personally appeared Sergey Savastouk personally
known to me or proved to me on the basis of satisfactory evidence to be the person(s) whose
name(s) is(are) subscribed to the within instrument and acknowledged to me that he/she/they
executed the same in his/her/their authorized capacity(ies), and that by his/her/their
signature(s) on the instrument the person(s), or the entity upon behalf of which the person(s)
acted, executed the instrument.

WITNESS my hand and official seal.




SIGNATURE OF NOTARY

SCHEDULE A

| Attorney Docket No. | Title | Application No. | Filing Date | Patent No. | Issue Date |
|---------------------|--|-----------------|-------------------|------------|--------------------|
| P-4532 US | Integrated Circuits and Methods For Their Fabrication | 60/030425 | October 29, 1996 | | |
| M-4532 US | Integrated Circuits and Methods For Their Fabrication | 09/083927 | May 22, 1998 | 6184060 | February 6, 2001 |
| M-4532-2D US | Integrated Circuits and Methods For Their Fabrication | 09/466535 | December 17, 1999 | | |
| M-4532-3D US | Integrated Circuits and Methods For Their Fabrication | 09/538869 | March 29, 2000 | 6420209 | July 16, 2002 |
| M-4532-5D US | Integrated Circuits and Methods For Their Fabrication | 09/752802 | December 28, 2000 | 6448153 | September 10, 2002 |
| M-4532-6P US | Integrated Circuits and Methods For Their Fabrication | 09/876888 | June 6, 2001 | 6498074 | December 24, 2002 |
| M-4532-8P US | Integrated Circuits and Methods For Their Fabrication | 10/059898 | January 28, 2002 | | |
| M-4532-9C US | Integrated Circuits and Methods For Their Fabrication | 10/133595 | April 26, 2002 | | |
| M-4532-10D US | Integrated Circuits and Method for Their Fabrication | 10/318833 | December 12, 2002 | | |
| M-4738 US | Plasma Generation and Plasma Processing of Materials | 08/781568 | January 9, 1997 | 5767627 | June 16, 1998 |
| M-5136 US | Plasma Processing Methods and Apparatus | 08/975403 | November 20, 1997 | 6139678 | October 31, 2000 |
| M-5136-1D US | Plasma Processing Methods and Apparatus | 09/519281 | March 7, 2000 | 6323134 | November 27, 2001 |
| M-5136-2D US | Plasma Processing Methods and Apparatus | 09/542519 | April 3, 2000 | | |
| M-5441 US | Article Holders and Holding Methods | 09/041284 | March 11, 1998 | 6095582 | August 1, 2000 |
| M-5480 US | Holders Suitable to Hold Articles During Processing And Article Processing Methods | 09/038642 | March 10, 1998 | 6168697 | January 2, 2001 |
| M-7551 US | Brim and Gas Escape for Non-Contact Wafer Holder | 09/457042 | December 7, 1999 | 6203661 | March 20, 2001 |

SCHEDULE A

| | Title | Application No. | Filing Date | Patent No. | Issue Date |
|--------------|---|-----------------|-------------------|------------|--------------------|
| M-7551-1D US | Brim and Gas Escape for Non-Contact Wafer Holder | 09/757242 | January 8, 2001 | | |
| M-7552 US | Non-Contact Workpiece Holder | 09/456135 | December 7, 1999 | 6402843 | June 11, 2002 |
| M-7553 US | Dynamic Break for a Non-Contact Wafer Holder | 09/456094 | December 7, 1999 | 6398823 | June 4, 2002 |
| M-7553-1D | Method of Preventing Motion of Article in an Article Holder | 10/020405 | December 14, 2001 | 6448188 | September 10, 2002 |
| M-7554 US | Plasma Processing Comprising Three Rotational Motions of an Article Being Processed | 09/713137 | November 14, 2000 | | |
| M-7555 US | Plasma Processing Methods And Apparatus | 09/315122 | May 19, 1999 | 6287976 | September 11, 2001 |
| M-7555-1D US | Plasma Processing Methods And Apparatus | 09/617211 | July 17, 2000 | 6261375 | July 17, 2001 |
| M-7556 US | Plasma Generator Ignition Circuit | 09/465989 | December 16, 1999 | 6121571 | September 19, 2000 |
| M-7561 US | Package of Integrated Circuits and Vertical Integration | 09/456225 | December 6, 1999 | 6322903 | November 27, 2001 |
| M-7561-1D US | Packaging of Integrated Circuits and Vertical Integration | 09/716092 | November 16, 2000 | | |
| M-9244 US | Detection and Handling of Semiconductor Wafers and Wafer-Like Objects | 09/632236 | August 4, 2000 | | |
| M-9244-1D US | Detection and Handling of Semiconductor Wafers and Wafer-Like Objects | 10/116462 | April 2, 2002 | | |
| M-9252 US | Non-Contact Workpiece Holder Using Vortex Chuck With Central Gas Flow | 09/633086 | August 4, 2000 | 6427991 | August 6, 2002 |